



# GL250N04B8

## GL Silicon N-Channel Power MOSFET

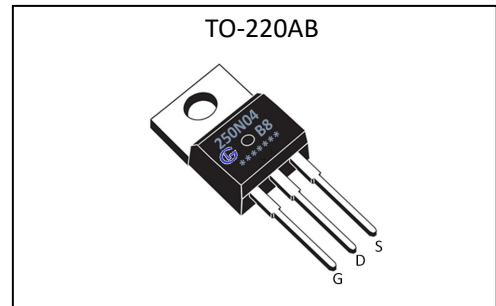
### General Description

The GL250N04B8 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications. The package form is TO-220AB, which accords with the RoHS standard.

$V_{DSS}$	40	V
$I_D$	250	A
$P_D$	242	W
$R_{DS(ON)type}$	2.1	m $\Omega$

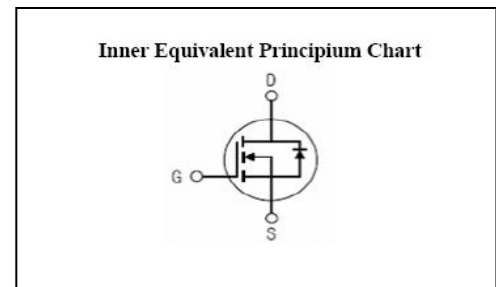
### Features

- $R_{DS(ON)} < 2.7m\Omega @ V_{GS}=10V$  (Typ2.1m $\Omega$ )
- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation



### Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



### Absolute (Tc= 25°C unless otherwise specified)

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	40	V
$I_D$	Continuous Drain Current	250	A
$I_{DM}$	Pulsed Drain Current	1000	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$P_D$	Power Dissipation	242	W
$E_{AS}$	Single pulse avalanche energy	2200	mJ
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	175, -55 to 175	$^{\circ}C$

Caution Stresses greater than those in the "Absolute Maximum Ratings" may cause permanent damage to the device

### Thermal Characteristics

Symbol	Parameter	Typ.	Units
$R_{\theta JC}$	Junction-to-Case <sup>a2</sup>	0.62	$^{\circ}C/W$



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### Electrical Characteristics (T<sub>c</sub>= 25°C unless otherwise specified)

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	--	--	V
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> =32V, V <sub>GS</sub> = 0V, T <sub>a</sub> =25°C	--	--	1.0	μA
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> = +20V	--	--	0.1	μA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> = -20V	--	--	-0.1	μA

ON Characteristics <sup>a3</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =80A	--	2.1	2.7	mΩ
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	--	4.0	V

Pulse width tp ≤ 380μs, δ ≤ 2%

Dynamic Characteristics <sup>a4</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =20V f=1.0MHz	--	5500	--	pF
C <sub>oss</sub>	Output Capacitance		--	900	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	290	--	

Resistive Switching Characteristics <sup>a4</sup>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>DD</sub> =20V, I <sub>D</sub> =80A V <sub>GS</sub> =10V, R <sub>G</sub> =1.6Ω, R <sub>L</sub> =0.5Ω	--	19	--	ns
t <sub>r</sub>	Rise Time		--	65	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	49	--	
t <sub>f</sub>	Fall Time		--	31	--	
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> =20V, I <sub>D</sub> =80A V <sub>GS</sub> =10V	--	75	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	23	--	
Q <sub>gd</sub>	Gate to Drain ( "Miller" )Charge		--	25	--	



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### Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{rr}$	Reverse Recovery Time	$I_S=240A, T_j=25^\circ C$	--	77	--	ns
$Q_{rr}$	Reverse Recovery Charge	$di_F/dt=100A/\mu s, V_{GS}=0V$	--	68	--	$\mu C$
$I_S$	Continuous Source Current <sup>a2</sup> (Body Diode)		--	--	250	A
$V_{SD}$	Diode Forward Voltage <sup>a3</sup>	$I_S=80A, V_{GS}=0V$	--	--	1.2	V

<sup>a1</sup>: Repetitive Rating: Pulse width limited by maximum junction temperature.

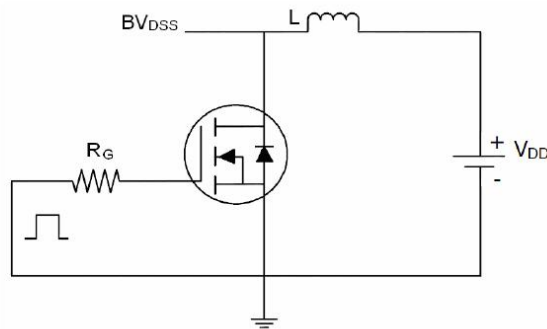
<sup>a2</sup>: Surface Mounted on FR4 Board,  $t \leq 10sec$ .

<sup>a3</sup>: Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .

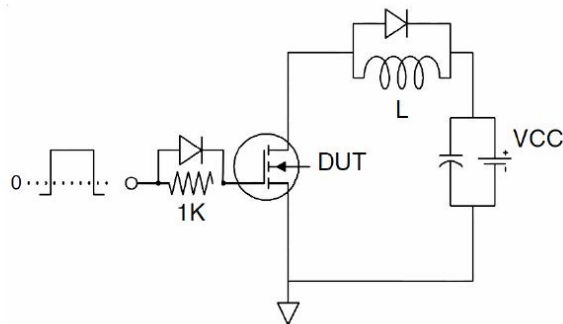
<sup>a4</sup>: Guaranteed by design, not subject to production

### Test Circuits

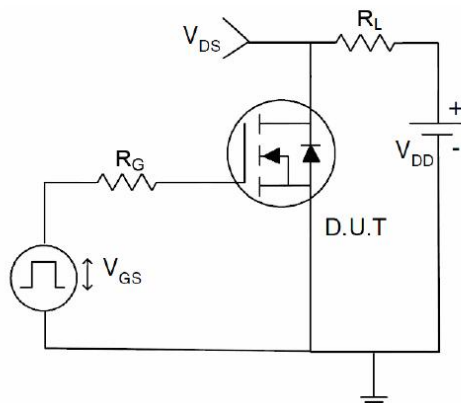
#### 1) EAS test Circuit



#### 2) Gate charge test Circuit



#### 3) Switch Time Test Circuit

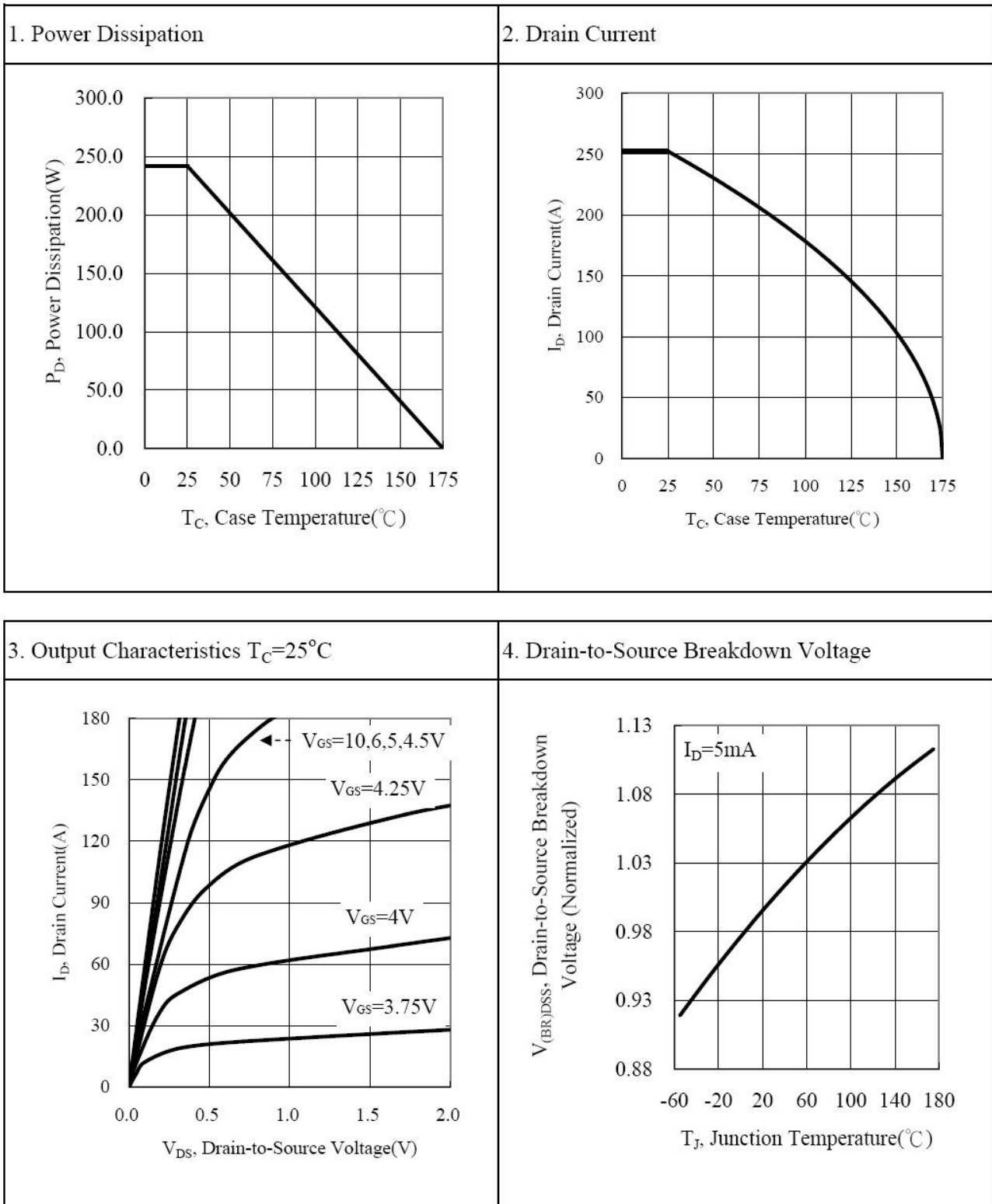




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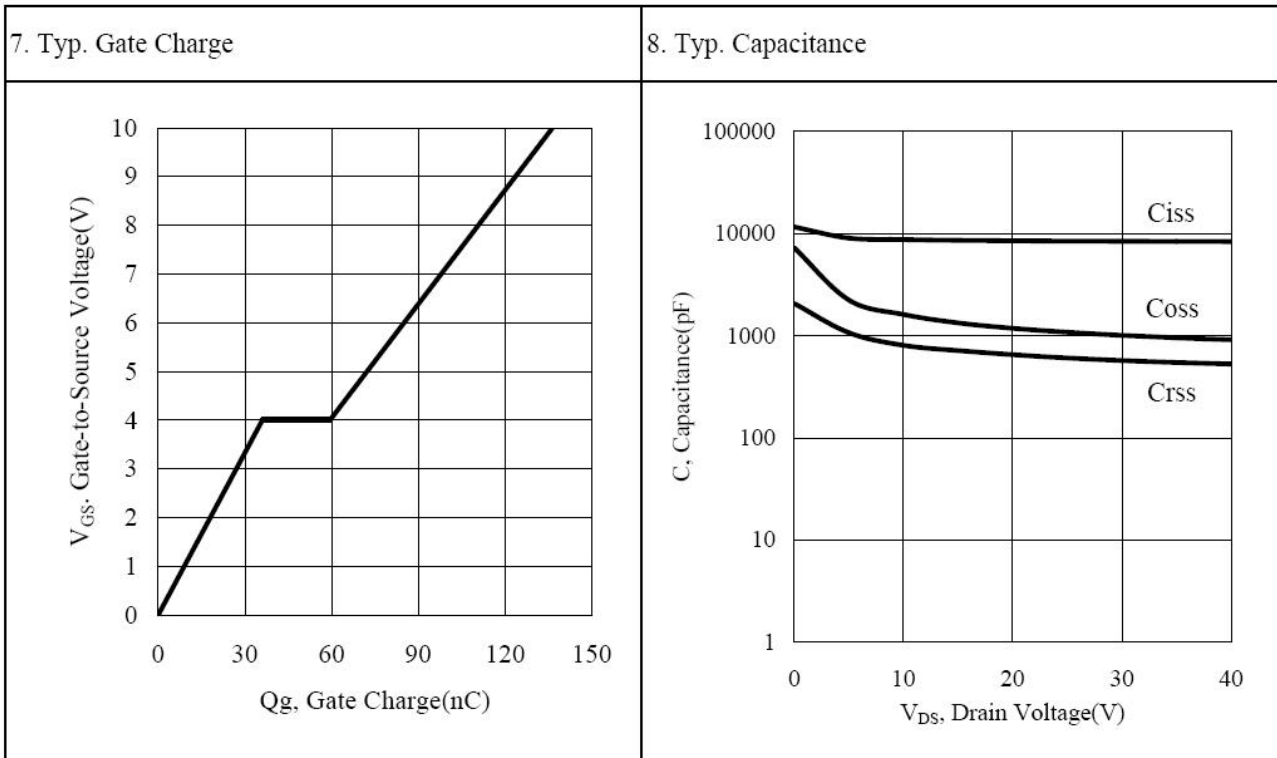
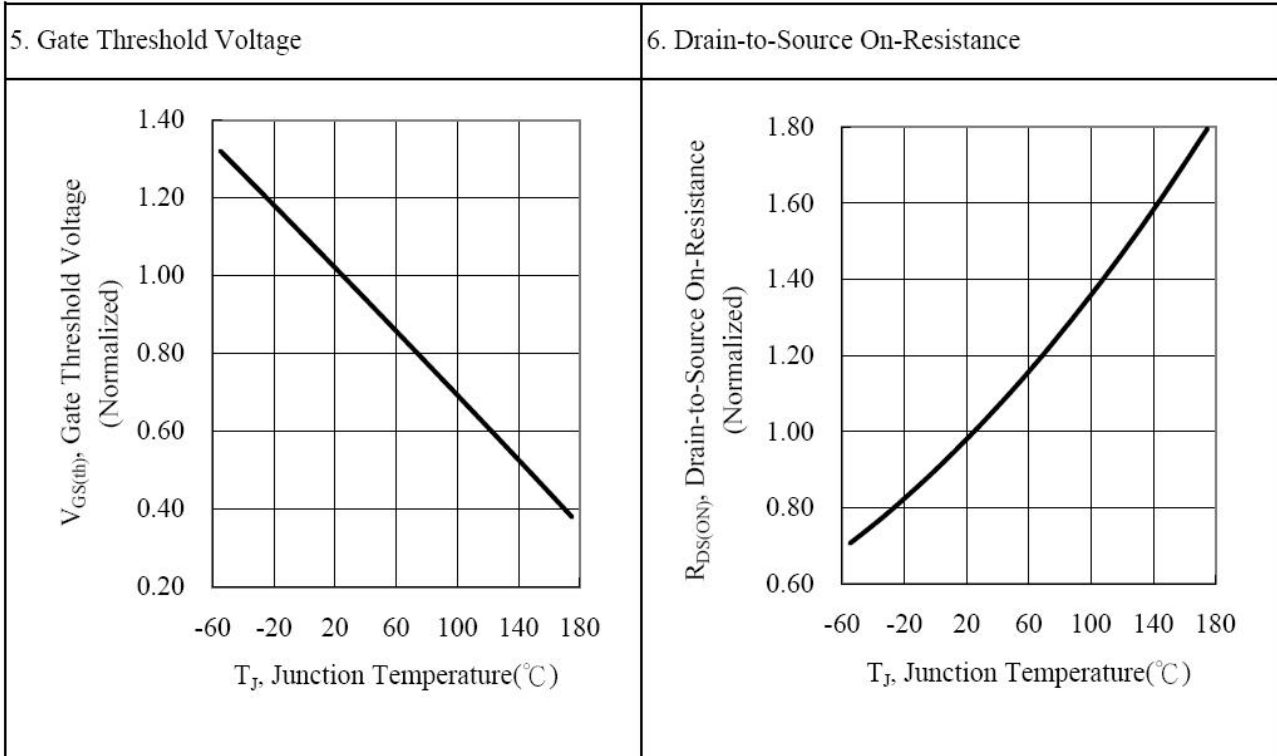
### Characteristics Curves





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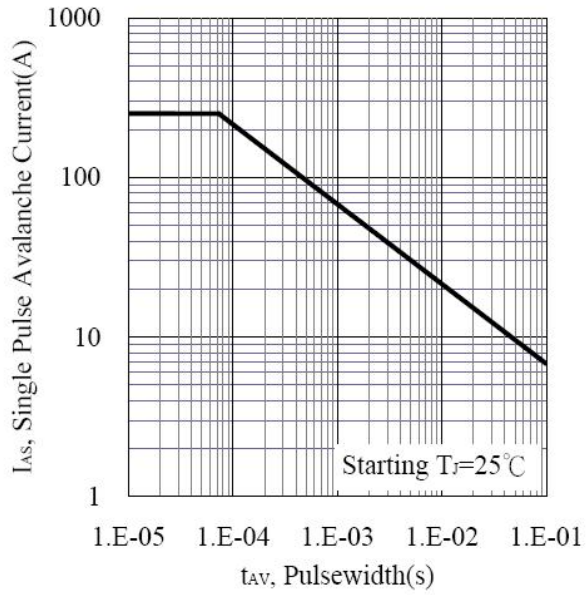




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9. Avalanche Characteristics



10. Forward Characteristics of reverse diode

